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Your Search was:

Last Name = SIM First Name = KIAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10670975	Not Issued	019		STITCHED MICRO-VIA TO ENHANCE ADHESION AND MECHANICAL STRENGTH	SIM, KIAN SIN
10673605	Not Issued	061		DIFFUSION BARRIER LAYER FOR LEAD FREE PACKAGE SUBSTRATE	SIM, KIAN SIN
10691135	Not Issued	030	10/21/2003	ROBUST INTERLOCKING VIA	SIM, KIAN SIN
10917140	Not Issued	041	08/11/2004	ROBUST INTERLOCKING VIA	SIM, KIAN SIN

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Last Name = SIR

First Name = JIUN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10691135	Not Issued	030	10/21/2003	ROBUST INTERLOCKING VIA	SIR, JIUN HANN
10882468	Not Issued	030	06/30/2004	INTERCONNECTS WITH DIRECT METALIZATION AND CONDUCTIVE POLYMER	SIR, JIUN HANN
10902446	Not Issued	030	1	INTERCONNECTS WITH INTERLOCKS	SIR, JIUN HANN
10917140	Not Issued	041	08/11/2004	ROBUST INTERLOCKING VIA	SIR, JIUN HANN

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